

**Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)**  
**Think Excellence. Live Excellence.**  
**Shri Vaishnav<sup>SM</sup> Institute of Technology and Science, Indore**  
**Department of Electricals and Electronics Engineering**



**Time Table - Jul-Dec 2021**

Doc No. : SVVV/AC/002		Rev No. :			Rev. Date :	
Class : II Yr. EC-IOT		Block: Department of Electrical and Electronics Engineering			Room No. : Online Mode	
Effective From: 23 Aug 2021		Total Load / Week: 24			Class Teacher: Mr. Ravindra Sharma	
TIME/DAY	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
<b>L1</b>	BTEC302	ML301	BTEC302	BTEC302	BTECIOT301	BTEC305
10:30 - 11:15	AK	VJ	AK	AK	RS	NP1
<b>L2</b>	BTECIOT302	BTEC303	BTEC305	BTEC303	ML301	BTEC302 T
11:15 - 12:00	LB	DM	NP1	DM	VJ	AK
<b>L3</b>	BTEC307 P	BTECIOT301	BTEC302 P	ML301	BTEC305	BTECIOT301 P
12:00 - 12:45	RS	RS	AK	VJ	NP1	RS
<b>L4</b>	BTEC303	BTEC305 P	BTEC305 T	BTEC303 P	BTEC303	BTEC307 P
12:45 - 01:30	DM	NP1	NP1	DM	DM	RS

**L-Lecture, T-Tutorial, P-Practical**

S No.	Sub. Code	Subject Name	L	T	P	C	Name of Faculty	Abb
1	ML301	Environment and Energy Studies	3	0	0	3	Mr. Vikas Jain	VJ
2	BTEC302	Network Analysis and Synthesis	3	1	2	5	Mr. Akash Kanugo	AK
3	BTEC303	Electronic Measurement and Instrumentation	3	1	2	5	Mr. Deepak Mittal	DM
4	BTEC305	Electronic Circuits	3	1	2	5	Mr. Naresh Purohit	NP1
5	BTECIOT301	Introduction to IOT and Embedded Systems	2	0	2	3	Mr. Ravindra Sharma	RS
6	BTECIOT302	Personality Development	0	0	2	1	Mr. Lalit Bhanwrela	LB
7	BTEC307	PCB Designing Lab	0	0	4	2	Mr. Ravindra Sharma	RS
			<b>14</b>	<b>3</b>	<b>14</b>	<b>24</b>		

**Mr. Shyam Gehlot**  
**Mr. Vikas Jain**  
**Time Table Coordinator**  
**Dept. of EEE**

**Dr. Namit Gupta**  
**HOD, Dept. of EEE**  
**Director, SVITS**

**Dr. Shishir Jain**  
**Time Table Coordinator**  
**SVVV**